

U.S. Application No. 09/889,380
Reply to Office Action of May 3, 2006

PATENT
450106-02849

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application. An identifier indicating the status of each claim is provided.

Listing of Claims

1. (currently amended) A digital signal processing apparatus, comprising:
 - a plurality of digital signal processing blocks including at least a signal processing block for decoding data of streams, each of said plurality of digital signal processing blocks having a ~~signal processor~~ general-purpose Central Processing Unit;
 - a host processing block for controlling said digital processing apparatus by outputting a command of a high layer and not on a real time basis; and
 - a bus for connecting said host processing block and said plurality of digital signal processing blocks for transferring said command and for transferring said data of streams,

wherein ~~each of said signal processors~~ Central Processing Units of each of said digital signal processing blocks interprets and executes said command, and

wherein data may be assigned priority in such a manner that streams of video data and streams of audio data that are assigned high priority are transmitted at high speed.

U.S. Application No. 09/889,380
Reply to Office Action of May 3, 2006

PATENT
450106-02849

2. (currently amended) The digital signal processing apparatus as set forth in claim 1,
wherein said plurality of digital signal processing blocks include at least a front end block for processing a received signal of a digital broadcast, and
wherein one of said plurality of digital signal processing blocks is a plug-in interface block for connecting external hardware.

3 - 5. (canceled)

6. (original) The digital signal processing apparatus as set forth in claim 1,
wherein the command is described and embedded in a script of hypertext,
wherein the hypertext is interpreted by a browser and a picture for operating the extension function is displayed, and
wherein a command corresponding to the function is embedded and displayed in the picture for operating the extension function.

7. (original) The digital signal processing apparatus as set forth in claim 1,
wherein the data of streams contains video data and / or audio data.

8. (original) The digital signal processing apparatus as set forth in claim 7,
wherein the video data and / or the audio data has been compressed.

U.S. Application No. 09/889,380
Reply to Office Action of May 3, 2006

PATENT
450106-02849

9. (original) The digital signal processing apparatus as set forth in claim 1,
wherein said bus is a general-purpose bus, and
wherein each block connected to said bus can be added or substituted.

10. (original) The digital signal processing apparatus as set forth in claim 9,
wherein when each block connected to said bus is added or substituted,
software for operating the added or substituted block is automatically installed.

11. (original) The digital signal processing apparatus as set forth in claim 9,
wherein software for operating the added or substituted block is stored in a
memory thereof, and
wherein when the block is added or substituted, the software stored in the
memory is installed.

12. (original) The digital signal processing apparatus as set forth in claim 9,
wherein when each block connected to said bus is added or substituted, a
service center is accessed through a telephone line, software for operating the added or
substituted block is downloaded from the service center through the telephone line, and the
downloaded software is installed.

U.S. Application No. 09/889,380
Reply to Office Action of May 3, 2006

PATENT
450106-02849

13. (currently amended) A digital signal processing method, comprising
the steps of:

structuring functions necessary for processing a digital signal as a plurality of
digital signal processing blocks and a host processing block, including at least a signal
processing block for decoding data of streams, each of said plurality of digital signal
processing blocks having a general-purpose Central Processing Unit signal processor and a
host processing block;

connecting the host processing block and the plurality of digital signal
processing blocks through a bus; and

outputting and transferring a command through the bus for controlling said
digital signal processing apparatus, said command being of a high layer and not on a real time
basis;

wherein each of said signal processors Central Processing Units of each of said
digital signal processing blocks interprets and executes said command and outputs said data of
streams through the bus, and

wherein data may be assigned priority in such a manner that streams of video
data and streams of audio data that are assigned high priority are transmitted at high speed.

14. (currently amended) The digital signal processing method as set forth
in claim 13,

wherein the plurality of digital signal processing blocks include at least a front
end block for processing a received signal of a digital broadcast, and

U.S. Application No. 09/889,380
Reply to Office Action of May 3, 2006

PATENT
450106-02849

wherein one of said plurality of digital signal processing blocks is a plug-in interface block for connecting external hardware.

15 - 17. (canceled)

18. (original) The digital signal processing method as set forth in claim 13, wherein the command is described and embedded in a script of hypertext.

19. (original) The digital signal proccsing method as set forth in claim 13, wherein the data of streams contains video data and / or audio data.

20. (original) The digital signal processing method as set forth in claim 19, wherein the video data and / or the audio data has been compressed.

21. (original) The digital signal processing method as set forth in claim 13, wherein the bus is a general-purpose bus, and wherein each block connected to the bus can be added or substituted.

22. (original) The digital signal proccsing method as set forth in claim 21, wherein when each block connected to the bus is added or substituted, software for operating the added or substituted block is automatically installed.

U.S. Application No. 09/889,380
Reply to Office Action of May 3, 2006

PATENT
450106-02849

23. (original) The digital signal processing method as set forth in claim 21,
wherein software for operating the added or substituted block is stored in a
memory thereof, and

wherein when the block is added or substituted, the software stored in the
memory is installed.

24. (original) The digital signal processing method as set forth in claim 21,
wherein when each block connected to the bus is added or substituted, a
service center is accessed through a telephone line, software for operating the added or
substituted block is downloaded from the service center through the telephone line, and the
downloaded software is installed.